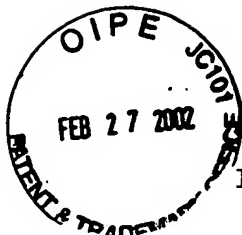


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Receipt
PATENT #3/29/02



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application : John V. Reynders
Application No. : 10/027,886
Filed : October 19, 2001
For : EFFICIENT SYSTEM AND METHOD OF NODE AND
LINK INSERTION FOR DEADLOCK-FREE ROUTING
ON ARBITRARY
Examiner : Not Assigned
Attorney's Docket : SUNM-058XX

Group Art Unit: 2181

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By: David A. Dagg
David A. Dagg
Registration No. 37,809
Attorney for Applicant(s)

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Sir:

In reviewing the Filing Receipt (copy enclosed) for the
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noted:

Efficient system and method of node and link insertion for
deadlock-free routing on **arbitrary**

Application No.: 10/027,886
Filed: October 19, 2001
Group Art Unit: 2181

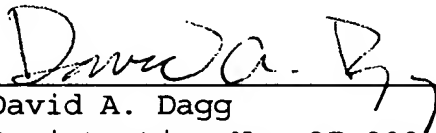
should read:

Efficient system and method of node and link insertion for
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Kindly correct the error of the Patent and Trademark Office
and issue a corrected Filing Receipt.

Respectfully submitted,

By:


David A. Dagg
Registration No. 37,809
Attorney for Applicant(s)

WEINGARTEN, SCHURGIN,
GAGNEBIN & LEOVICI LLP
Ten Post Office Square
Boston, Massachusetts 02109

Telephone: (617) 542-2290

Telecopier: (617) 451-0313

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/027,886	10/19/2001	2181	1196	P6270	10	22	8

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Applicant(s)

John V. Reynders, Newton, MA;
Radia J. Perlman, Acton, MA;
Guy L. Steele JR., Lexington, MA;
Dah Ming Chiu, Acton, MA;
Miriam C. Kadansky, Westford, MA;
Murat Yuksel, Troy, NY;

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Assignment For Published Patent Application

Sun Microsystems, Inc.;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 02/05/2002

Projected Publication Date: 04/24/2003

Non-Publication Request: No

Early Publication Request: No

Title

Efficient system and method of node and link insertion for deadlock-free routing on arbitrary

arbitrary

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Preliminary Class
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